

PRODUCT DATASHEET

Nano Fuse · Surface Mount

JFC0402TS TIME-LAG FUSE



JFC0402TS TIME-LAG FUSE



Description -

JFC0402TS Series are time-lag fuse, The chip fuses set the industry standard for performance, reliability and quality. The solder-free design provides excellent on-off and temperature cycling characteristics and also makes our chip fuses more heat and shock tolerant than typical subminiature fuses.

Features

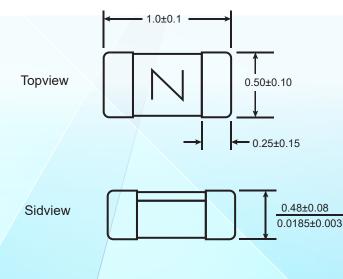
- Time-lag for excessive current
- Compatible with reflow and wave solder
- Ceramic and glass construction
- Excellent environmental integrity
- One time positive disconnect
- Lead Free and Halogen free material

Electrical Characteristics

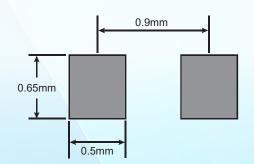
Rated Current	1.0In	2.5In	3.5In
1A~4A	4 hour min.	30 sec max.	-
200mA~750mA	4 fiour finiti.	-	30 sec max.

Dimensions

Drawing not to scale (Unit:mm/inch)



Recommended land pattern:



Print solder in thickness of 0.08mm to 0.10mm

JDT FUSE Industrial Co., Ltd www.jdtfuse.com Page 1/3 Revised 09/17/21



Performance Specifications

Part No.	Rated Current (A)	Rated Voltage DC	Interrupting Rating*	Resistance (mΩ)Typ**	Typical Melt I²t (A²sec)***
JFC0402-0200TS	0.20			2130	0.00068
JFC0402-0250TS	0.25			1352	0.0013
JFC0402-0315TS	0.315			936	0.0016
JFC0402-0375TS	0.375			713	0.0021
JFC0402-0500TS	0.50			458	0.0047
JFC0402-0750TS	0.75	32V	35A	202	0.013
JFC0402-1100TS	1.0			123	0.045
JFC0402-1150TS	1.5			73.0	0.071
JFC0402-1200TS	2.0			36.0	0.15
JFC0402-1250TS	2.5			22.0	0.23
JFC0402-1300TS	3.0			16.0	0.37
JFC0402-1350TS	3.5			12.0	0.51
JFC0402-1400TS	4.0			9.8	0.68

* DC Interrupting Rating (Measured at rated voltage, time constant of less than 50 microseconds, battery source)

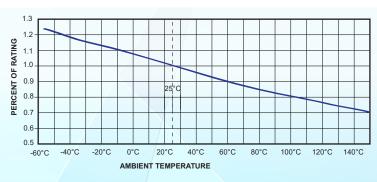
** DC Cold Resistance are measured at <10% of rated current in ambient temperature of 25°C

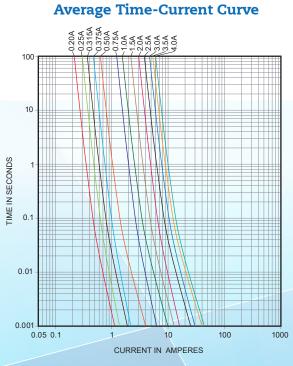
* * *Typical Melting I²t (Measured with a battery bank at rated DC voltage, Measured at 1ms open time, time constant of calibrated circuit less than 50 microseconds).

Environmental Characteristic

- Normal ambient temperature: 23+/-3°C
- Operating temperature: -55°C ~ 150°C, with proper correction factor applied

Temperature Derating Curve

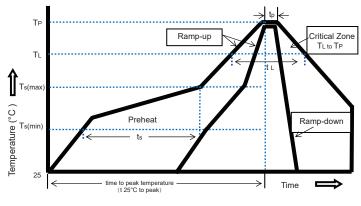




JDT FUSE Industrial Co., Ltd www.jdtfuse.com



JFC0402TS TIME-LAG FUSE



Soldering Parameters

lem	25	- time to peak temperature		Average ramp u
	1 \$	(t 25°C to peak)	> Time	Liquidous tempe Time at liquidou
	Solde	ring Method	Parameter	Peak package b
	Wave solder	Reservoir temperature	260°C	Time (t⊵) within calssification ter
		Time in reservoir	10 Secs max	Average ramp-o
	Infrared reflow	Temperature	260°C	Time (25°C to F
		Time	30 Secs max	

1	Profile Feature	Lead(Pb) free solder
	Temperature min (T _{smin})	150°C
Preheat and soak	Temperature max (T _{smax})	200°C
	Time (T _{smin} to T _{smax})(ts)	60-120 Secs
Average ran	ramp up rate Tsmax to Tp 3°C/Secs Max	
Liquidous te Time at liqui	mperature(T∟) dous(t∟)	217°C 60-150 Secs
Peak packa	Peak package body temperature (TP) 260°C	
	nin 5°C of the specified temperaturea(Tc)	30 Secs
Average ran	rage ramp-down rate (TP to Tsmax) 6°C/Secs Max	
Time (25°C	to Peak Temperature)	8 Minutes Max

Packing

	No. Quantity &Packaging Code	
	JFC0402TS	10000 fuses/reel (8mm tape-and-reel on a 7 inch (178mm) reel per EIA Standard 481)
Part Numbering System		
	JF	C 0402 - xxxx T S
		S=SMD
		T=Time-Lag xxxx=四位电流
0402 Series		
		C=Ceramic JF=JDT Fuse

- If in use beyond the requirements of the specifications, must pass through the mutual confirmation !
- If the specification is not appropriate, must through consultation between the two sides and by the company to modify.

OTHERS

It could be in conformance with another file which made by our company.

JDT FUSE Industrial Co., Ltd